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Patent

JUN 22 2005Customer No. 31561
Docket No. 9788-US-PA
Application No.: 10/065,874**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Applicant : Chia-Tien Peng
Application No. : 10/065,874
Filed : 2002/11/27
For : METHOD FOR FABRICATING POLYSILICON LAYER
Examiner : Fuller, Eric B
Art Unit : 1762

INFORMATION DISCLOSURE STATEMENT
002-1-703-872-9306 (Total pages by fax: 1+1+2+4)

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Enclosed are a PTO Form 1449 listing 1 reference of the above-identified application, a copy of the aforesaid reference, and its English translation. Pursuant to 37 CFR §1.56, Applicant is submitting the reference in compliance with his duty of disclosure. The Examiner is requested to make this reference a citation of official record.

No fee is believed to be due for this submission. However, the Commissioner is authorized to charge any fees required in connection with the submission of the IDS reference to Account No. 50-2620 (Order No.: 9788-US-PA)

The submission of the reference should not be interpreted as admitting it as prior art.

Respectfully Submitted,
JIANQI SHYUN Intellectual Property Office

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FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICATION	ATTY. DOCKET NO.: 9788-US-PA	APPLICATION NO.: 10/065,872
	APPLICANT: Chia-Tien Peng	
	FILING DATE: November 27, 2002	GROUP 1762

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
	61-240676	October 25, 1986	JP			April 18, 1985

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

EXAMINER	DATE CONSIDERED
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